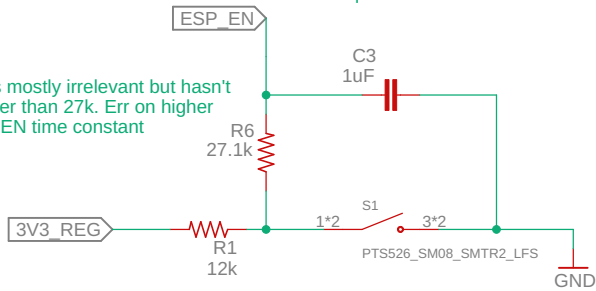


At LEAST 1uF cap for EN to hold it low long enough during FTDI auto-program sequence

Resistor value is mostly irrelevant but hasn't been testing lower than 27k. Err on higher side to increase EN time constant



Repurposed JTAG pins for I2C bus and 2 epdiy pins. Don't know if JTAG will work with I2C strong-ish pullups and might mess up BQ. Switch between the two functions with a FET or regain these pins for exclusive use by moving others to IO expander in future rev.



Default unpopulated.
 Exists in case FT232 programming does not work

No cap needed for IO0

